

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Cheng, et al. Docket No.: TSM02-1262
Serial No.: 10/602,241 Art Unit: 2812
Filed: June 24, 2003 Examiner: TBD
For: Method for Manufacturing a Semiconductor Device Having an Improved Disposable Spacer

Certificate of Mailing via First Class Mail (37 C.F.R. § 1.8(a))

Date of Deposit: March 16, 2004

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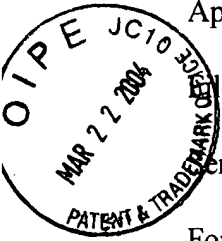
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Respectfully submitted,

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Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

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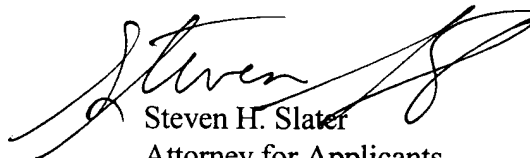
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Respectfully submitted,

March 16, 2004

Date


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10/602,241

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Cheng, et al.

2812

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TSM02-1262

[illegible][illegible]Date
Considered

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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